

D 11207

Library
25/11/2018

(Pages : 2)

Name.....

Reg. No.....

FIFTH SEMESTER B.C.A. DEGREE EXAMINATION, NOVEMBER 2016

(CUCBCSS-UG)

BCA 5B 12—MICROPROCESSOR AND APPLICATIONS

Time : Three Hours

Maximum : 80 Marks

Part A

Answer all questions.

Each question carries 1 mark.

1. The _____ carries information to and from the processor.
2. In microprocessor _____ is used to hold the address of the next instruction.
3. 8086 has _____ datalines.
4. 8086 is designed to operate on _____ modes.
5. The BIU uses a mechanism known as an instruction stream queue to implement _____.
6. The non-maskable interrupt is initiated by _____ pin.
7. _____ is used to speed up program execution by overlapping instruction fetch and execution.
8. _____ is responsible for performing all external bus operations.
9. The minimum bus cycle containing four microprocessor clock periods is called _____.
10. in the I/O mode, the 8255 ports work as _____.

(10 × 1 = 10 marks)

Part B

Answer all questions.

Each question carries 2 marks.

11. What is the difference between microprocessor and microcomputer ?
12. Define macros.
13. What is meant by direct address mode ?
14. What is modular programming ?
15. Explain programmed I/O.

(5 × 2 = 10 marks)

Turn over

Part C

*Answer any five questions.
Each question carries 4 marks.*

16. Explain various status flags in 8086.
17. Explain the concept of subroutine (procedure).
18. Explain the concept of stack in 8086.
19. What is DMA data transfer ?
20. Explain the concept of assembler macros.
21. What are the features of 80286 ?
22. Write an assembly language program for 8086 to find the largest 8 bit number in a data array.
23. What is polling ?

(5 × 4 = 20 marks)

Part D

*Answer any five questions.
Each question carries 8 marks.*

24. Explain various status flags in 8086.
25. Explain the concept of BIOS.
26. What are the features of 8259 ?
27. Explain hardware and software interrupts.
28. What are the different addressing modes used in 8086 ?
29. Explain different registers in 8086 microprocessor.
30. Explain different modes 8255 operation.
31. Draw and discuss the architecture of 8086. Mention the jobs performed by BIU and EU.

(5 × 8 = 40 marks)